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APR 67



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Qiyong Bian

Examiner: Thai Q. Phan

Serial No.: 09/941,068

Group Art Unit: 2128

Filed: August 28, 2001

Docket: 303.741US1

Title: CO-SIMULATION OF VERILOG/PLI AND SYSTEM C MODULES USING
REMOTE PROCEDURE CALL

AMENDMENT UNDER 37 C.F.R. § 1.312

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the Notice of Allowance and Notice of Allowability mailed April 20, 2006,
please amend as follows: